TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (U-MOSVI-H)

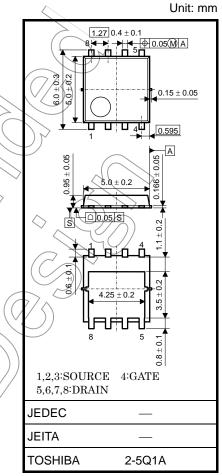
TPCA8036-H

High-Efficiency DC-DC Converter Applications Notebook PC Applications Portable Equipment Applications

- Small footprint due to a small and thin package
- High-speed switching
- Small gate charge: QSW = 13 nC (typ.)
- Low drain-source ON-resistance: $RDS(ON) = 2.8 \text{ m}\Omega \text{ (typ.)}$
- High forward transfer admittance: $|\,Y_{\rm fs}\,|$ = 113 S (typ.)
- Low leakage current: $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 30 \ V)$
- Enhancement mode: $V_{th} = 1.3$ to 2.3 V ($V_{DS} = 10$ V, $I_D = 0.5$ mA)

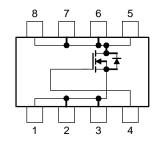
Absolute Maximum Ratings (Ta = 25°C)

			$\langle \bigcirc \rangle$	\sim
Characteristic		Symbol	Rating	⊖ _{Unit}
Drain-source voltage		V _{DSS}	30	V
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V _{DGR}	30	V
Gate-source voltage		VGSS	<u>+</u> 20	< <v< td=""></v<>
Drain current	DC (Note 1)	ID	38	A
	Pulsed (Note 1)	LDP	114	
Drain power dissipation (Tc=25°C)			45	W
Drain power dissipation (t = 10 s) (Note 2a)		PD	2.8	W
Drain power dissipation $(t = 10 s)$ (Note 2b)		PD <	1.6	w
Single-pulse avalanche energy (Note 3)		EAS	188	mJ
Avalanche current		I _{AR}	38	А
Repetitive avalanche energy (Tc = 25°C) (Note 4)		EAR	0.18	mJ
Channel temperature		Tch	150	°C
Storage temperature range		Tstg	-55 to 150	°C



Weight: 0.069 g (typ.)

Circuit Configuration



Note: For Notes 1 to 4, refer to the next page.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the

reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.

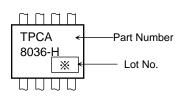
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(b) Device mounted on a glass-epoxy board (b)

Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case (Tc=25°C)	R _{th (ch-c)}	2.78	°C/W
Thermal resistance, channel to ambient $(t = 10 \text{ s})$ (Note 2a)	R _{th (ch-a)}	44.6	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R _{th (ch-a)}	78.1	°C/W

Marking (Note 5)



- Note 1: Ensure that the channel temperature does not exceed 150°C.
- Note 2: (a) Device mounted on a glass-epoxy board (a)
- FR-4 FR-4 25.4 × 25.4 × 0.8 $25.4\times25.4\times0.8$ (Unit: mm) (Unit: mm) (a) (b) Note 3: $V_{DD} = 24 \text{ V}, T_{eh} = 25^{\circ}\text{C}$ (initial), L = 100 μ H, $R_G \neq 25^{\circ}\Omega$, IAR = 38 A Note 4: Repetitive rating: pulse width limited by maximum channel temperature Note 5: * Weekly code: (Three digits) Week of manufacture (01) for the first week of the year, continuing up to 52 or 53) Year of manufacture (The last digit of the year)

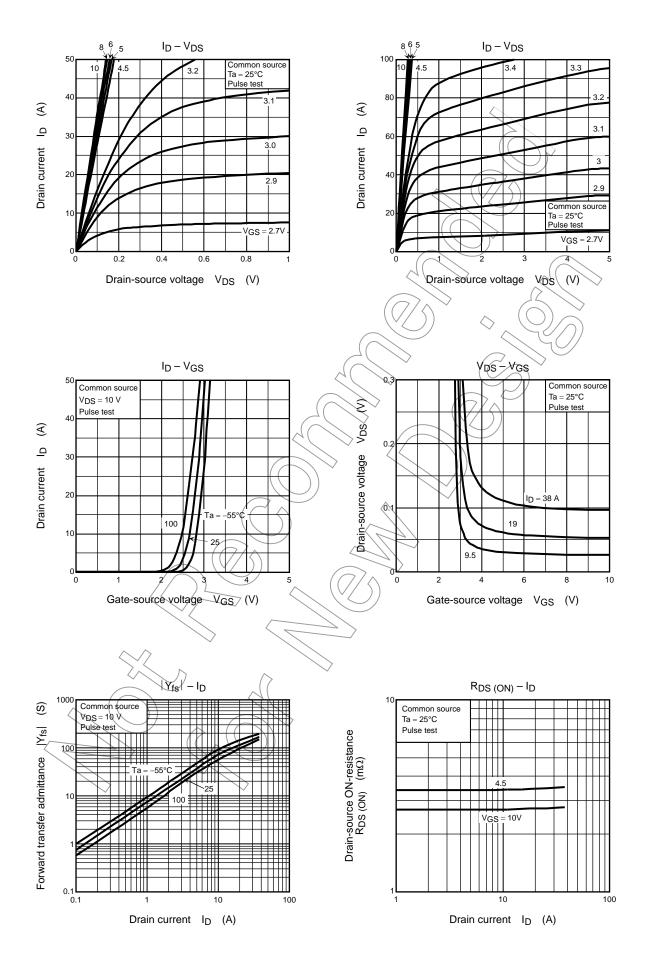
Electrical Characteristics (Ta = 25°C)

Ch	aracteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS}=\pm 20~V,~V_{DS}=0~V$	_	—	±100	nA
Drain cutoff curre	nt	I _{DSS}	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			10	μΑ
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	30	—	_	V
		V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	15		_	v
Gate threshold vo	bltage	V _{th}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 0.5 \text{ mA}$	1.3		2.3	V
Drain-source ON-resistance		R _{DS (ON)}	$V_{GS} = 4.5 \text{ V}, I_D = 19 \text{ A}$		3.4	4.8	mΩ
			$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 19 \text{ A}$	\mathcal{A}	2.8	4.2	
Forward transfer	admittance	Y _{fs}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 19 \text{ A}$	57	113	_	S
Input capacitance		C _{iss}			3500	4600	
Reverse transfer capacitance		C _{rss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	230	370	pF
Output capacitance		C _{oss}			690	\searrow	
Gate resistance		rg	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-6	1.0) 1.5	Ω
Switching time	Rise time	tr	10V 10 ID = 19A	X	4.7) _	
	Turn-on time	t _{on}		$\langle \widehat{(} $	> 14		ns
	Fall time	t _f			7.7		ns
	Turn-off time	toff	$V_{DD} \approx 15 V$ Duty $\leq 1\%$, t _W = 10 µs	_	48	—	
Total gate charge (gate-source plus gate-drain)		Qg	V _{DD} ≈ 24 V, V _{GS} = 10 V, I _D = 38 A	_	50	_	
			$V_{DD} \approx 24 \text{ V}, V_{GS} = 5 \text{ V}, V_{D} \neq 38 \text{ A}$		26	_	
Gate-source char	rge 1	Q _{gs1}			11	_	nC
Gate-drain ("Mille	r") charge	Qgd	$V_{DD} \approx 24 V, V_{GS} = 10 V, I_D = 38 A$		7.8	_	
Gate switch charg	ge (()/	QSW			13	_	

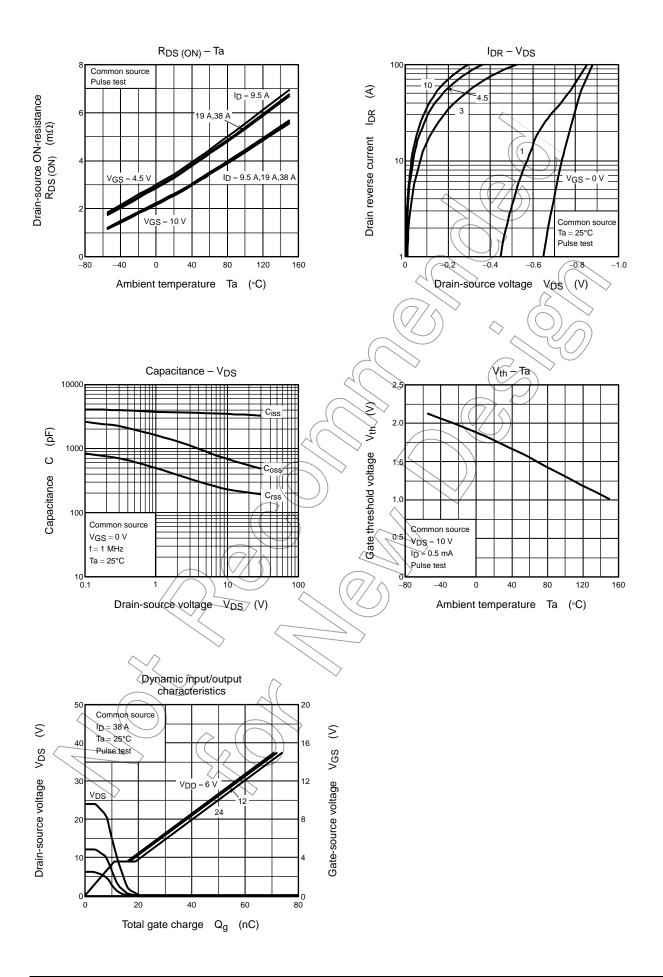
Source-Drain Ratings and Characteristics (Ta = 25° C)

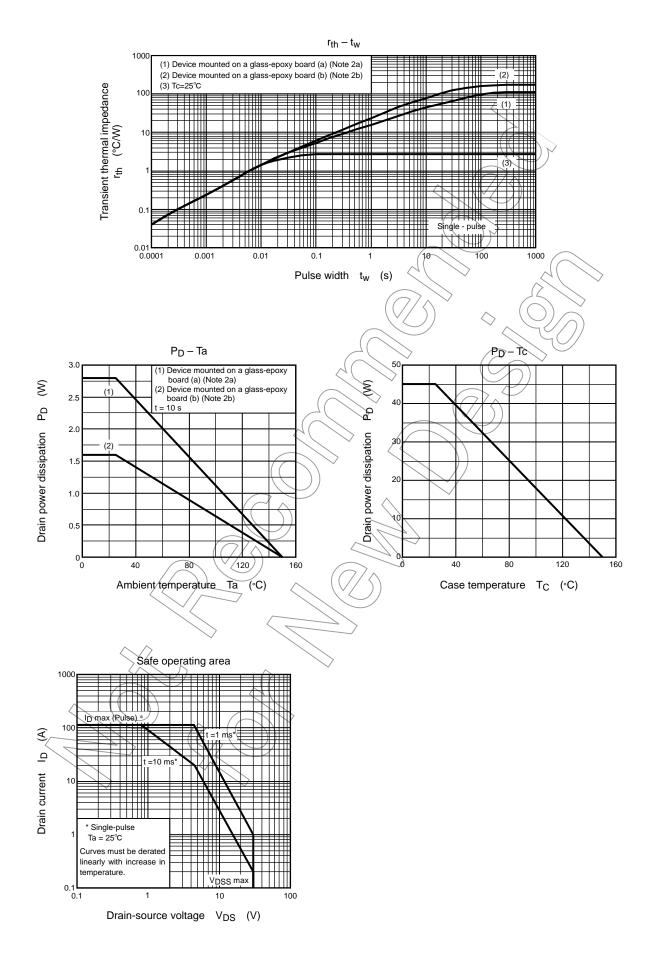
Characteristic	Symbol Test Condition	Min	Тур.	Max	Unit
Drain reverse current Pulse (Note 1)		—	_	114	А
Forward voltage (diode)	V_{DSF} $I_{\text{DR}} = 38 \text{ A}, V_{\text{GS}} = 0 \text{ V}$			-1.2	V

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