#### RECOMMENDED PCB LAYOUT FOR QSH-XXX-01-X-D-EM2 REVISION K 1/14/2014 ECN-219036 .0050 [0.127] SOLDER MASK DEFINED DIMENSION $.0430^{+.003}_{-.001}$ 1.092 $^{+0.08}_{-0.03}$ .030 [0.76] .075[1.91] .050[1.27] REF - .1685±.003 [4.280±0.08] - .7875[20.003] <del>-</del> .125[3.18] .1850 4.699 HIDDEN LINES REFER TO COPPER PAD - .012[0.30] .0100[0.254] REF WIDTH OF PLATED EDGE DETAIL 'A' # OF BANKS X .7875 [20.003] +.0225 [.5715] SCALE 5: (SEE NOTE 3) -EM2: .064±.004 [1.63±0.10] PCB K .0100 [0.254] 01 (SEE NOTE 4) <del>}</del> -BROKEN EDGE MUST BE LOWER THAN PLATED EDGE OR .5713[14.511] 29 EQUAL SPACES @ .0197 [.500] MILLED FOR HOUSING CLEARANCE. (SEE NOTE 1) DETAIL "B"-"B" SCALE 10:1 NOTES: 1. 1 BANK = 30 POSITIONS/SIDE FOR STANDARD VERSION. 2. NOTE DELETED. 3. PLATED EDGE MAY BE DIVIDED UP IN BANKS DEPENDING UPON DESIGNED K 4. REVIEW THE "EDGE MOUNT Q1 INTERFACES" ON THE PROCESSING LITERATURE PROPRIETARY NOTE SECTION OF SAMTEC.COM. THIS DOCUMENT CONTAINS INFORMATION CONFIDENTIAL AND PROPRIETARY TO SAMTEC, INC. AND SHALL NOT BE REPRODUCED OR TRANSFERRED TO OTHER DOCUMENTS OR

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code 55322 SHEET 1 OF 2

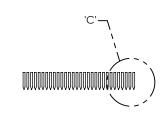
# REVISION K

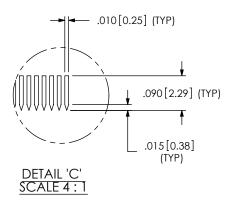
(SEE NOTE 1, SHEET 1)

## RECOMMENDED STENCIL LAYOUT FOR QSH-XXX-01-X-D-EM2

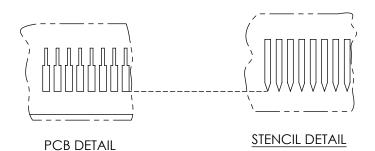
NOTES: 1. STENCIL TO BE .0060[.152] THICK 2. NOTE DELETED.

.7875 [20.003] .5713[14.511] 29 EQUAL SPACES @ .0197 [.500]





### STENCIL APPLICATION SCALE 6:1 ALIGN EDGES AS SHOWN



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